PATENT Conf. No.: 5248

IN THE UNITED STATES PATENT OFFICE

Applicants:

William C. Black et al.

Assignee:

Xilinx, Inc.

Title:

"Analog Front-End Having Built-In Equalization and Applications

Thereof"

Serial No.:

10/659,803

File Date:

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Examiner:

Ted M. Wang

Art Unit:

2611

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X-1356 US

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5248

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RESPONSE TO RESTRICTION REQUIREMENT

Dear Sir:

In response to the Restriction Requirement mailed from the Patent Office on date, Applicants elect, with traverse, to prosecute Group II: Claims 1-8 and 16-23.

Applicants hereby withdraw Claims 9-15 and 24-30 (Group I) from prosecution. Applicants reserve the right to request rejoinder under MPEP 821.04, if appropriate, and/or to prosecute any future cancelled claims in a subsequent divisional application, without prejudice.

A listing of the claims in the present application is appended hereto, with current status.

Respectfully submitted,

Michael R. Hardaway Attorney for Applicants

Reg. No. 52,992

I hereby certify that this correspondence is being filed via EFS-Web with the United States Patent & Trademark Office-on December 17, 2007.

By: Pat Tompkins